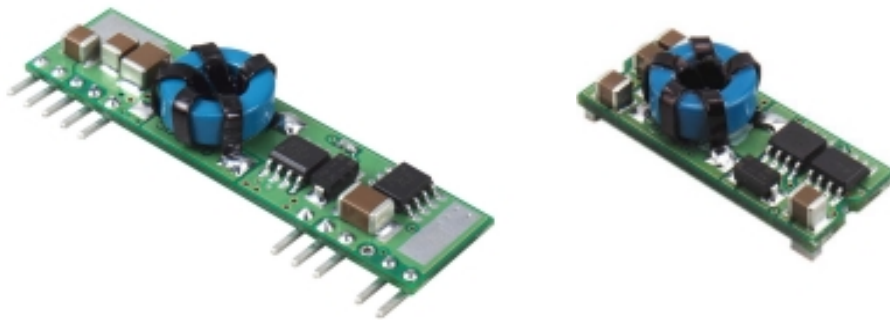


DC/DC Power-On-Point (POP[®]) Regulator ERS 15 Ampere SIL/SMD Series



- DC/DC non-isolated regulator modules
- Industry standard pinning
- Continuous short circuit proof
- High efficiency up to 94%
- Very low output ripple and noise
- Over temperature protection
- Single-in-line footprint or surface mounted
- External output voltage adjust
- Remote on/off control

- DC/DC Regler ohne galvanische Trennung
- Industriestandard pinning
- Dauerkurzschlussfest
- Hoher Wirkungsgrad von bis zu 94%
- Sehr gute Werte von Rippel und Noise
- Übertemperaturschutz
- Single-in-line footprint oder surface mounted
- Ext. Ausgangsspannungsabgleich
- Inhibit

- Module convertisseur DC/DC sans séparation galvanique
- Pinning standard industriel
- Protection contre courts-circuits permanents
- Degré d'efficacité jusqu'à 94%
- Ondulation résiduelle de sortie très faible
- Protection de température trop élevée
- Single-in-line footprint ou SMD
- Ajustement externe de la tension sortie
- Fonction inhibit

Product range

Typenübersicht

Sommaire des types

PART NUMBER	INPUT VOLTAGE		INPUT CURRENT		OUTPUT		EFFICIENCY
	Nominal	Range	max. @ full load	No Load	Voltage	Current	Typical
ERS05-3V350	5 VDC	3...5.5 VDC (4.5...5.5VDC at 3.3Vout)	3.65..10.5 A*	60...70 mA*	0.9...3.6 VDC*	15 A	82%(1.0V) 84%(1.2V) 87%(1.5V) 88%(1.8V)
ERS05-3V350-S	5 VDC	3...5.5 VDC (4.5...5.5VDC at 3.3Vout)	3.65..10.5 A*	60...70 mA*	0.9...3.6 VDC*	15 A	89%(2.0V) 92%(2.5V) 94%(3.3V)

*) at nominal input voltage of 5VDC

ERS 05 - 3V3 50 x - S

Product Series

Nominal Input Voltage

Nominal Output Voltage
(3V3 = 3.3V)

Output Power in Watts

blank = SIL-11 pinning

S = SMD (surface mounted)

blank = positive logic inhibit on/off

N = negative logic inhibit on/off

Specifications

Spezifikationen

Spécifications

All values refer to an ambient temperature of 25°C and nominal rated values where nothing else is specified

INPUT SPECIFICATIONS

Characteristics		Conditions	min	typ	max	unit
U _{in}	Input voltage	T _A < T _{A max} ; U _{out} = 1.0 to 2.5Vdc	3.0		5.5	Vdc
		T _A < T _{A max} ; U _{out} = 3.3Vdc	4.5		5.5	Vdc
I _{nl}	No load current	I _{out} = 0; U _{in} = 1.0...1.5Vdc		60		mA
		I _{out} = 0; U _{in} = 1.8...3.3Vdc		70		mA
U _{in off}	Under voltage lockout (U _{in nom} = 5Vdc)	Power up (U _{in nom} = 5Vdc)		2.8		Vdc
		Power down (U _{in nom} = 5Vdc)		2.7		Vdc
I _{in max}	Full load input current	P _{out} = P _{max} (U _{out} = 1.0Vdc to 3.3Vdc)	3.65		10.53	A
		Reversed polarity protection		none		
	Inhibit on/off control (positive logic)	On (open circuit or open collector referenced to -U _{in})	t.b.d.		+Vin	Vdc
		Off (open collector referenced to -U _{in})			< 0.4	Vdc
	Inhibit on/off control (negative logic; add suffix "N" to part number)	On (open circuit or open collector referenced to -U _{in})			open circuit or < 0.4	Vdc
		Off (open collector referenced to -U _{in})	2.8		+Vin	Vdc

Note: it is recommended to put a 100uF capacitor (ESR<100mOhm) in parallel close to the input to reduce the input ripple voltage.

OUTPUT SPECIFICATIONS

Characteristics		Conditions	min	typ	max	unit
U _{acc}	Output voltage accuracy	of nominal output voltage			±1.5	% U _{out}
	Line regulation	I _{out} = I _{out nom}			±0.2	% U _{out}
	Load regulation	0% load up to 100% load			±0.5	% U _{out}
t _r	Load transient recovery time	10% to 100% step load change		t.b.d.		us
	Load transient error band			t.b.d.		% U _{out}
t _r	Ramp-up time	10% to 100% load; 10% to 90% U _{out nom}		t.b.d.		ms
t _s	Start-up time	Connection of input and until U _{out} = 90% U _{out nom}		t.b.d.		ms
T _{coeff}	Temperature coefficient			±0.03		%/°C
U _{out trim}	Output voltage adjustment	see "External output trim" page 8	0.9		3.63	Vdc
U _{r n}	Output ripple & noise	Measured with 10uF tantalum capacitor and 1uF ceramic capacitor across the output			50	mVpp

Characteristics		Conditions	min	typ	max	unit
C_{max}	Output capacitance	low ESR			10	mF
$I_{out\ max}$	Output current limit threshold	see current limit chart, page 4			150	% $I_{out\ nom}$
	Output short circuit characteristic	see short circuit protection graph, page 4	hiccup mode			
	Output short circuit protection			continuous		

GENERAL SPECIFICATIONS

Characteristics		Conditions	min	typ	max	unit
U_{iso}	Isolation voltage	input/output, input/case, output/case		none		Vdc
R_{iso}	Isolation resistance	Input to output		none		MOhm
$C_{I/O}$	Input/output capacitance	+Input to +output (-/- none)			t.b.d.	pF
f_s	Switching frequency	Fixed	270	300	330	kHz
	Approvals	File number E195564	UL / cUL1950			
	Case material		Open Frame			
	Weight			7		g
	Pinning	see "case" page 7				
	Dimensions	LxWxH, see "case" page 7	50.8 x 8.5 x 12.7			mm
	Soldering temperature (SIL-11)	peak temperature, maximum 10s preheating: temperature ramp 4°C/sec to 160°C, 25mm/sec.			260+/-5	°C
	Soldering temperature (SMD)	Reflow soldering	see "Soldering", page 9			°C

EMC SPECIFICATIONS

Characteristics		Conditions	min	typ	max	unit
	EMC conducted	EN 55022, see "EMC information" page 4	t.b.d.			

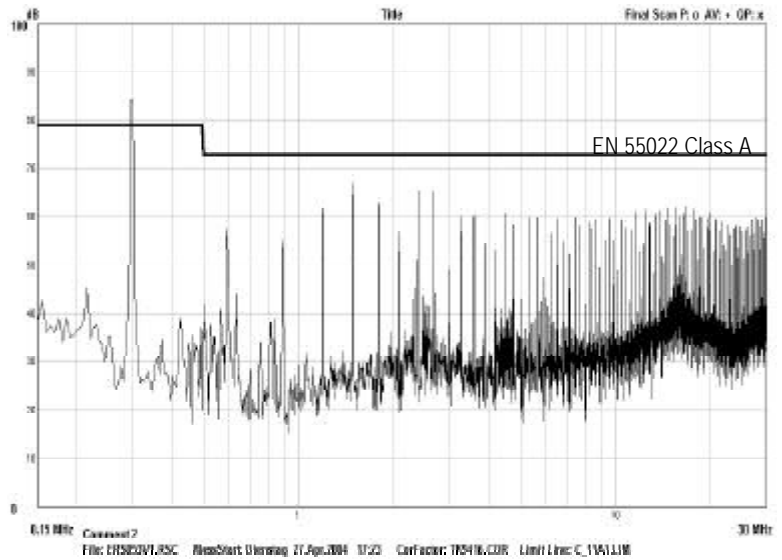
ENVIRONMENTAL SPECIFICATIONS

Characteristics		Conditions	min	typ	max	unit
	Vibration (sinusoidal)	Frequency 10-500 / 500-10 Hz Swep 2 / axis Duration 6 min / axis non operating	10			G
	Shock (half sinus)	Number of pulses 3 in 6 directions Pulse duration 11ms Wave form half sine wave non operating	40			G
T_A	Operating temperatures	Ambient temperature, see also "Derating" page 5	-40		+85	°C
	Storage temperatures	Ambient temperature	-55		+125	°C
	Thermal shutdown	Temperature at MOSFET (Q1), pin 6, see "Thermal shutdown", page 9	110	120	130	°C

EMC information conducted, ERS05-3V333:

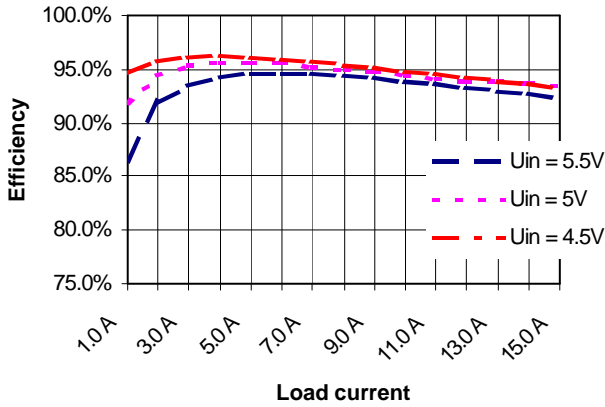
EMC tests conducted at full load. Input capacity of 100uF in parallel was used.

For further EMC requirements, please contact your local distributor / representative or contact Fabrimex directly.

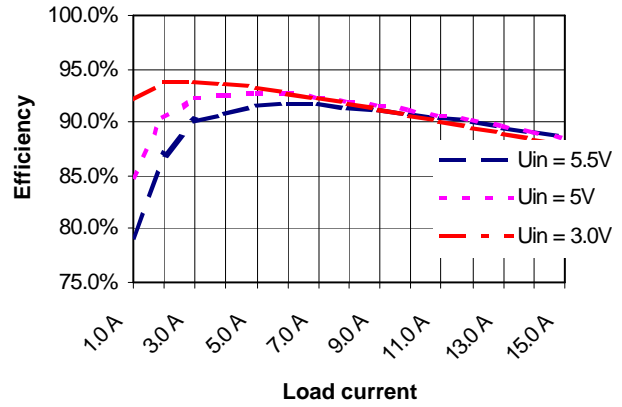


Typical characteristics

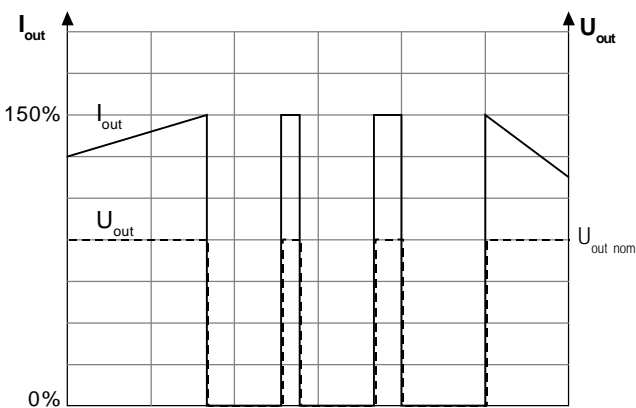
Efficiency $U_{out} = 3.3V$ (typical)



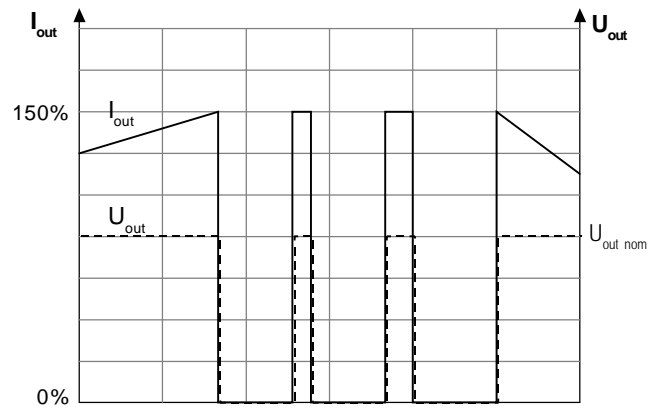
Efficiency $U_{out} = 1.8V$ (typical)



Short circuit protection (typical)



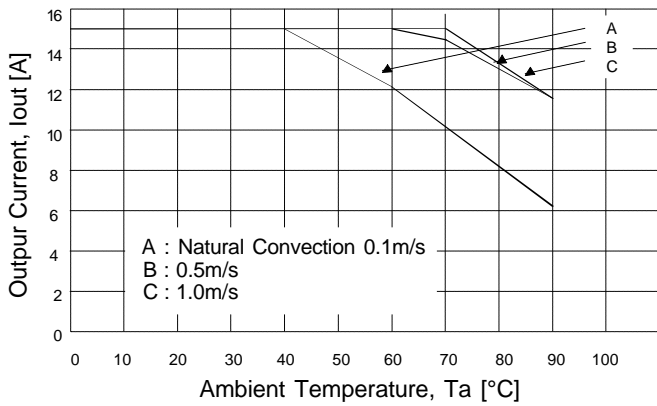
Current limit characteristic (typical)



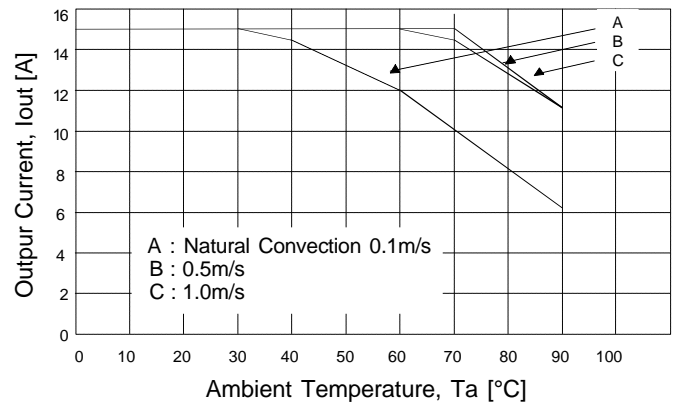
Derating ERS 15A Series, SIL-11

The operating ambient temperature range of ERS 15A series is -40°C to $+85^{\circ}\text{C}$. When operating the ERS series, proper derating or cooling is needed. The following curves are the derating curves of ERS 15A without heat sink. Please note that these are relative values in a test environment. Ambient temperature can not be exactly defined in an application.

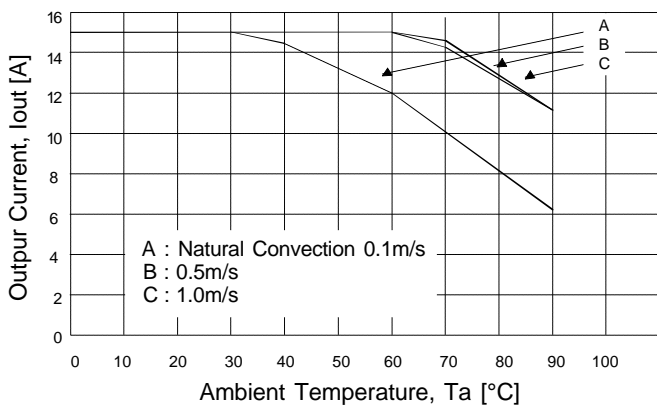
Derating ERS05-xx50 (SIL-11) at $U_{in} = 5\text{ Vdc}$, $U_{out} = 1.0\text{ Vdc}$:



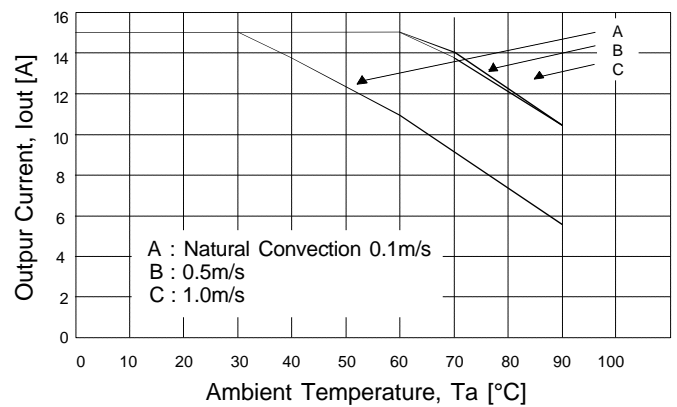
Derating ERS05-xx50 (SIL-11) at $U_{in} = 5\text{ Vdc}$, $U_{out} = 1.2\text{ Vdc}$:



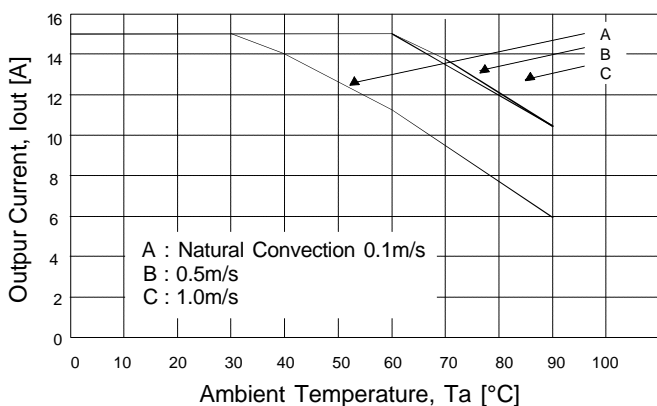
Derating ERS05-xx50 (SIL-11) at $U_{in} = 5\text{ Vdc}$, $U_{out} = 1.5\text{ Vdc}$:



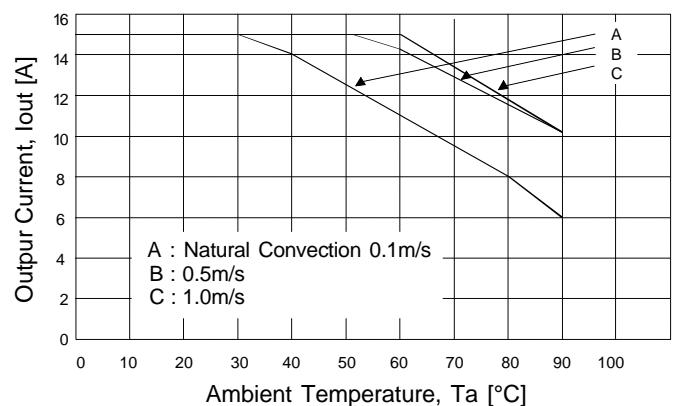
Derating ERS05-xx50 (SIL-11) at $U_{in} = 5\text{ Vdc}$, $U_{out} = 1.8\text{ Vdc}$:



Derating ERS05-xx50 (SIL-11) at $U_{in} = 5\text{ Vdc}$, $U_{out} = 2.5\text{ Vdc}$:



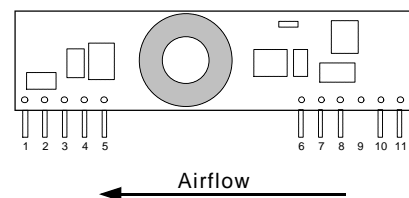
Derating ERS05-xx50 (SIL-11) at $U_{in} = 5\text{ Vdc}$, $U_{out} = 3.3\text{ Vdc}$:



Remark:

Fabrimec recommends a free space of at least 5mm on each side of the regulator to ensure an efficient air flow.

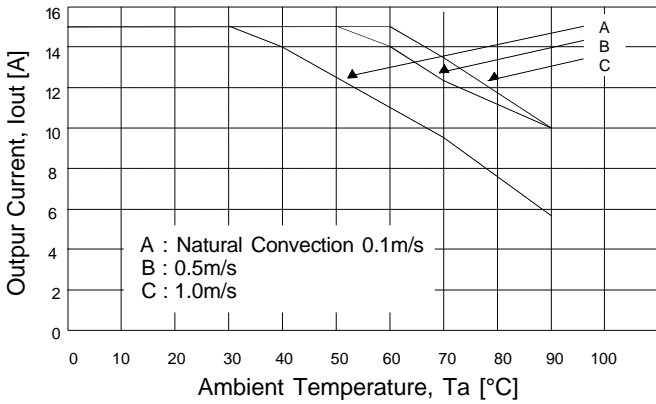
For efficient cooling, the placement of the regulator must be considered so that the direction of the airflow is as shown:



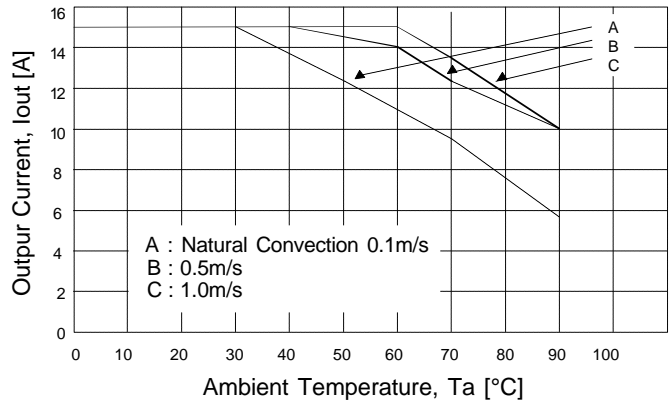
Derating ERS 15A Series, SMD

The operating ambient temperature range of ERS 15A series is -40°C to $+85^{\circ}\text{C}$. When operating the ERS series, proper derating or cooling is needed. The following curves are the derating curves of ERS 15A without heat sink. Please note that these are relative values in a test environment. Ambient temperature can not be exactly defined in an application.

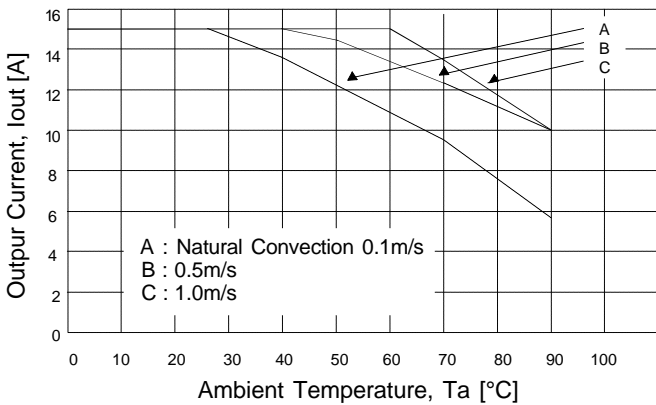
Derating ERS05-xx50-S (SMD) at $U_{in} = 5\text{ Vdc}$, $U_{out} = 1.0\text{ Vdc}$:



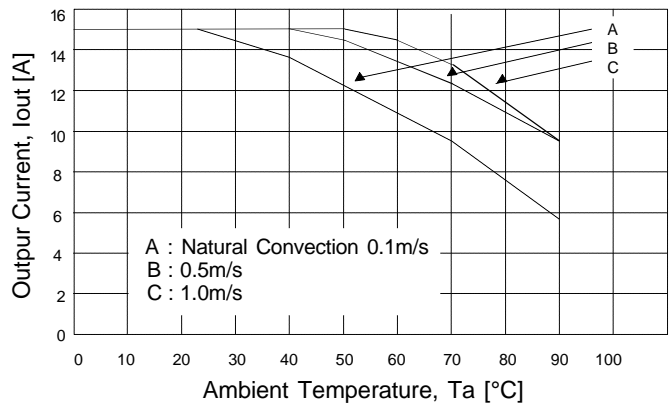
Derating ERS05-xx50-S (SMD) at $U_{in} = 5\text{ Vdc}$, $U_{out} = 1.2\text{ Vdc}$:



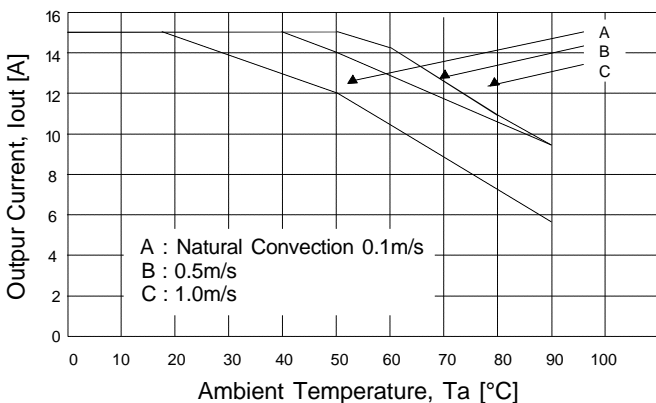
Derating ERS05-xx50-S (SMD) at $U_{in} = 5\text{ Vdc}$, $U_{out} = 1.5\text{ Vdc}$:



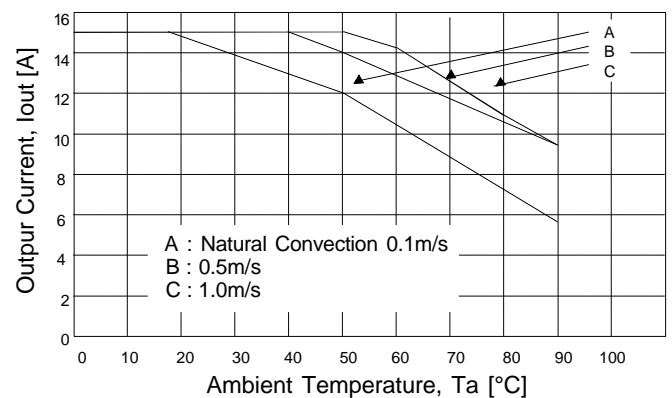
Derating ERS05-xx50-S (SMD) at $U_{in} = 5\text{ Vdc}$, $U_{out} = 1.8\text{ Vdc}$:



Derating ERS05-xx50-S (SMD) at $U_{in} = 5\text{ Vdc}$, $U_{out} = 2.5\text{ Vdc}$:



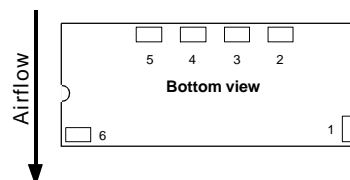
Derating ERS05-xx50-S (SMD) at $U_{in} = 5\text{ Vdc}$, $U_{out} = 3.3\text{ Vdc}$:



Remark:

FabrimeX recommends a free space of at least 5mm above the regulator to ensure an efficient air flow.

For efficient cooling, the placement of the regulator must be considered so that the direction of the airflow is as shown:



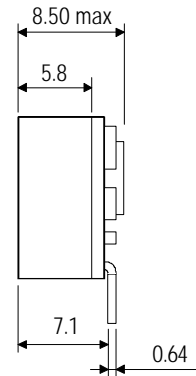
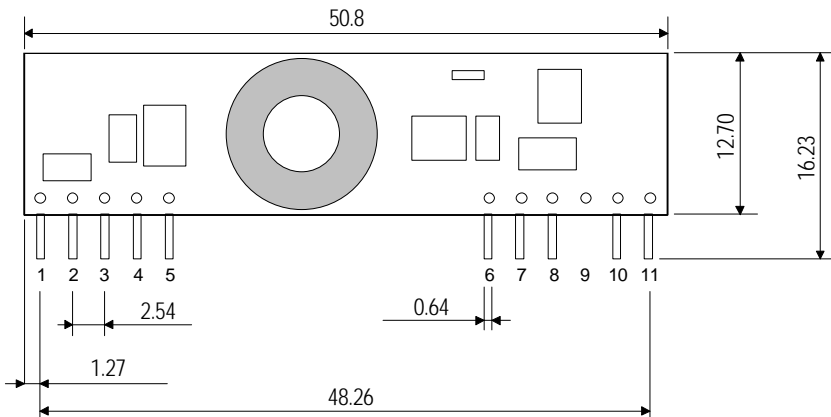
Case

Gehäuse

Boîtier

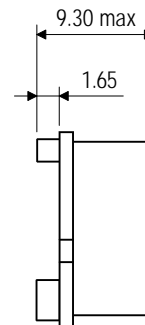
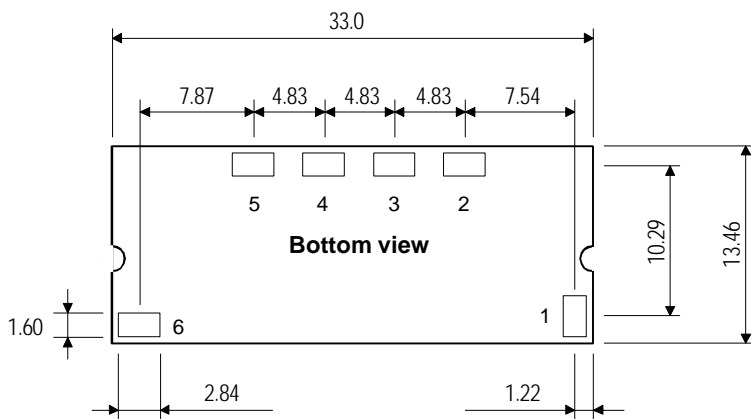
Normal tolerance 1/10 ±0.5 mm, 1/100 ±0.25 mm

ERS05-xx50 (SIL-11):



Pin	Function
1	+ Vout
2	+ Vout
3	+ sense
4	+ Vout
5	common
6	common
7	+ Vin
8	+ Vin
9	no pin
10	trim
11	on/off

ERS05-xx50-S (SMD):



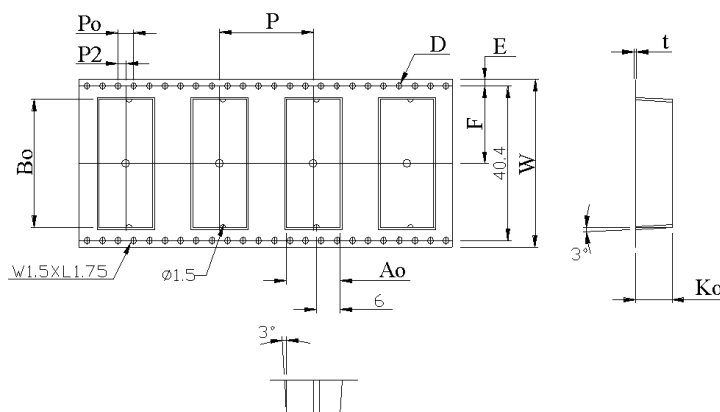
Pin	Function
1	on/off
2	+sense
3	trim
4	+ Vout
5	common
6	+ Vin

Packaging

Verpackung

Emballage

SMD Tape and Reel Dimensions

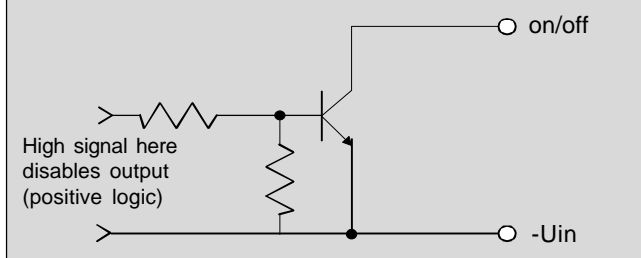


ITEM	SPEC
W	44.00 ^{+0.30} _{-0.30}
Ao	13.70 ^{+0.10} _{-0.10}
Bo	33.50 ^{+0.10} _{-0.10}
Ko	9.30 ^{+0.10} _{-0.10}
P	24.00 ^{+0.10} _{-0.10}
F	20.20 ^{+0.10} _{-0.10}
E	1.75 ^{+0.10} _{-0.10}
D	1.50 ^{+0.10} _{-0.00}
D1	2.00 ^{+0.25} _{-0.00}
Po	4.00 ^{+0.10} _{-0.10}
P2	2.00 ^{+0.10} _{-0.10}
t	0.40 ^{+0.05} _{-0.05}

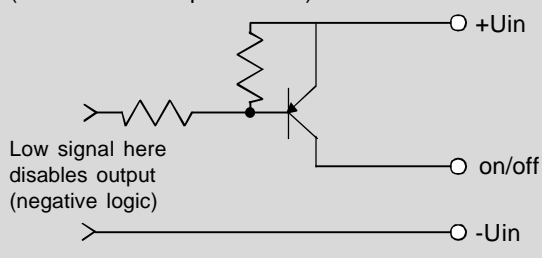
Inhibit on/off control

The ERS 15A allows the user to switch the module on and off electronically by inhibit on/off feature. If the control voltage exceeds 5.5Vdc then an external protective circuit has to be used similar to the following examples.

Application example positive logic:

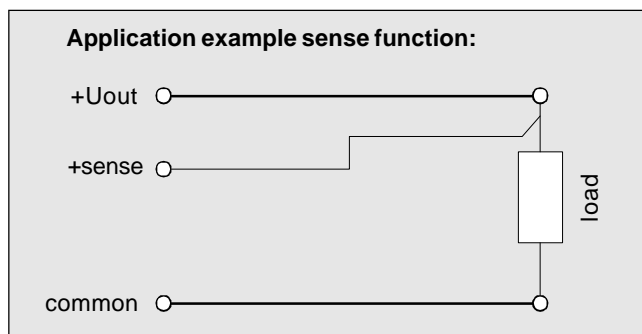


Application example negative logic: (add suffix 'N' to part number)



Remote sense

Application example sense function:



The ERS 15A allows the user to compensate a voltage drop between the regulator and the load of maximum 500mV by using the sense function. For proper operation the sense shall be connected as shown in the example, as close to the load as possible.

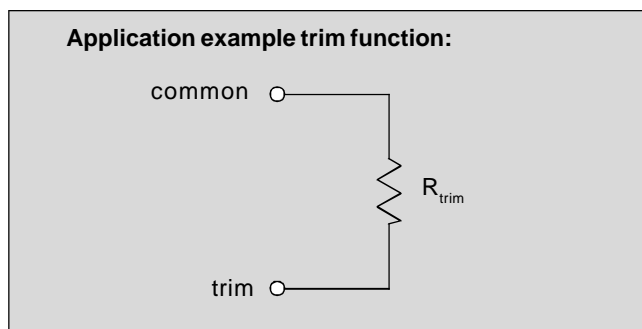
When the sense feature is not used, leave sense pin disconnected.

External output voltage trim

The ERS 15A trim function requires the user to adjust the output voltage between 0.9Vdc and 3.63Vdc by connecting an external resistor between the trim pin and the common pin.

Always use a resistor between the common pin and the trim pin!

Application example trim function:



U _{out}	R _{trim}
0.90 Vdc	135.36 kOhm
1.00 Vdc	79.17 kOhm
1.20 Vdc	41.71 kOhm
1.50 Vdc	22.98 kOhm
1.80 Vdc	14.96 kOhm
2.00 Vdc	11.75 kOhm
2.50 Vdc	6.93 kOhm
3.30 Vdc	3.15 kOhm
3.63 Vdc	2.20 kOhm

Thermal shutdown

The ERS 15A includes a temperature protection for safe operation. The temperature protection will shut down the regulator at typical 120°C at the pin 6 of the MOSFET's Q1 and Q2.

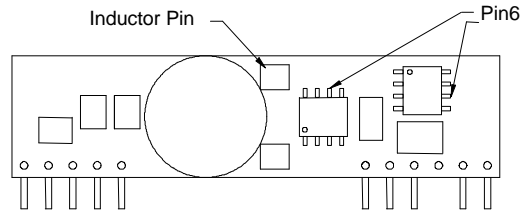
The regulator will automatically continue operation when the temperature at the pin(s) has dropped in the region of 40°C.

The temperature protection is a feature to enhance the safety in your application. Normal operation has to be designed so to stay within safe operation, temperature wise the pin temperature must stay below 110°C.

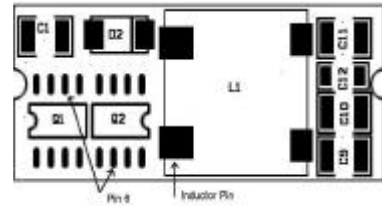
Frequent use of the temperature protection may result in damage of the regulator.

Please use the derating graphs to define the safe operating area in your application.

ERS05-xx50 (SIL-11)



ERS05-xx50-S (SMD)



Soldering

ERS05-xx50 (SIL-11), wave soldering:

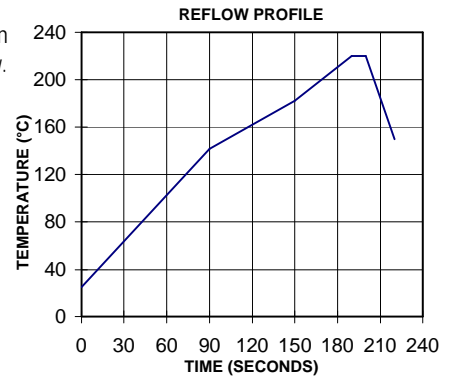
- Peak temperature: 260°C ±5°C
- Peak duration: 10sec
- Temperature ramp: 4°C/sec to 160°C
- Feed rate: 25mm/sec

Löten

ERS05-xx50-S (SMD), reflow soldering:

The following reflow profile is an example for a SMD hot air reflow.

- Peak temperature: 220°C
- Peak duration: 10sec

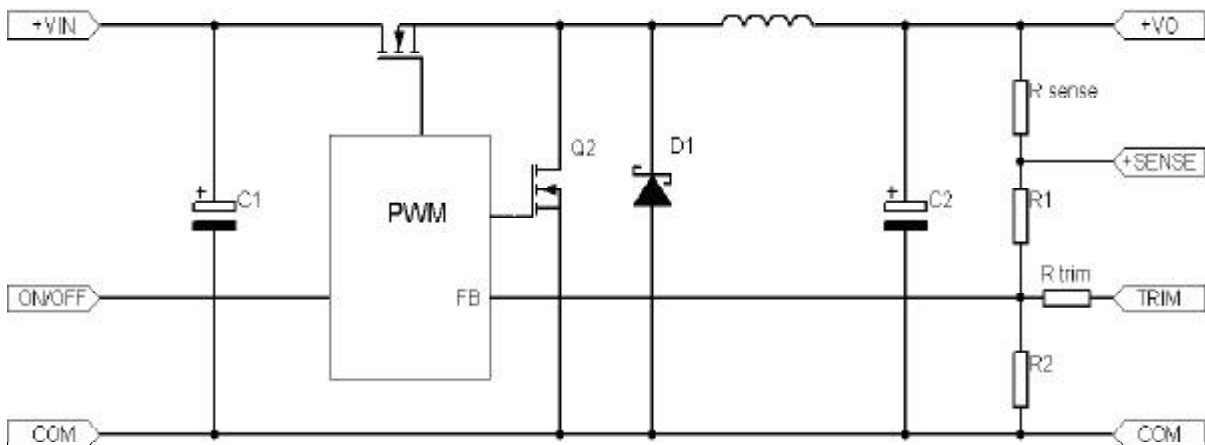


Functional Block Diagram

Blockschema

Synoptique

The ERS 15A series topology is based on a non-isolated synchronous buck converter. In a typical pre-bias application the regulator does not draw any reverse current at start-up.



Cleaning

The modules are cleanable with the today's known and in the electronics industry usually used products.

Due to the different cleaning processes and new available products, we highly recommend to do a compatibility test when using the converters the first time.

Waschen

Die Module sind waschbar mit den heute bekannten und in der Elektronikindustrie üblichen Reinigungsmitteln.

Bedingt durch die verschiedenen Reinigungsprozesse und neu auf den Markt kommende Mittel, raten wir dringend beim Ersteinsatz der Konverter eine Verträglichkeitsprüfung vorzunehmen.

Lavage

Les modules sont lavables avec les solvants couramment utilisés dans l'industrie électronique.

Dû aux différents processus de lavage et aux nouveaux détergents disponibles sur le marché, il est strictement recommandé de faire un test de compatibilité avant la première utilisation.

Notice: All statements, technical information, and recommendations related to FABRIMEX's products are based on information believed to be reliable, but the accuracy or completeness thereof is not guaranteed. Before utilizing the product, the user should determine the suitability of the product for its intended use.

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